

P-Channel 20-V (D-S) MOSFET

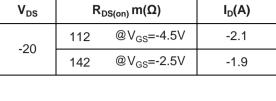
FEATURES

- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

PRODUCTY SUMMARY			
V _{DS}	$R_{DS(on)} m(\Omega)$		I _D (A)
-20	112	@V _{GS} =-4.5V	-2.1
-20	142	@V _{GS} =-2.5V	-1.9

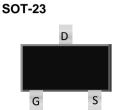
Application

- Portable Devices
- ■Consumer Electronics



Mechanical

●Case: SOT-23 Package



Packing Information

Package	Packing	
SOT-23	3Kpcs / 7" Reel	

Maximum Ratings (T _A =25°C unless otherwise specified)				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	-20	V	
Gate-Source Voltage	V _{GS}	±12	V	
Continuous Drain Current 1)	I _D	-2.1	А	
Maximum Power Dissipation	P _D	0.35	W	
Pulsed Drain Current 2)	I _{DM}	-8.4	А	
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to 150	°C	

Typical Thermal Resistance				
Parameter	Symbol	Limit	Unit	
Junction-to-Ambient Thermal Resistance	$R_{\theta JA}$	100	°C/W	

Note:

R0JA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper



Electrical Characteristics (T _A = 25°C UNLESS OTHERWISE NOTED)						
Characteristics	Symbol	Test Condition	Limits			l lmit
Characteristics			Min	Тур	Max	Unit
		Static				
Drain-Source Breakdown Voltage	B_{VDSS}	$V_{GS} = 0V, I_{D} = -250uA$	-20			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-0.4	-0.65	-1.2	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-4.5V, I _D =-2.1A	-	90	112	mΩ
		V _{GS} =-2.5V, I _D =-1.9A	-	110	142	mΩ
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V			-1	uA
Gate-Source Leakage Current	I _{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$			±100	nA
		Dynamic ³⁾				
Total Gate Charge	Q_g		-	7	-	nC
Gate-Source Charge	Q_{gs}	V_{DS} =-10V, I_{D} =-3.1A, V_{GS} =-4.5V	-	1	-	nC
Gate-Drain Charge	Q_{gd}	1 63	-	1.8	-	nC
Input Capacitance	C _{iss}		-	522	-	pF
Output Capacitance	C_{oss}	V_{DS} =-10V, V_{GS} =0V, f=1.0MHZ	ı	55	-	pF
Reverse Transfer Capacitance	C_{rss}		-	40	-	pF
		Switching				
Turn-On Delay Time	t _{d(on)}		_	10	_	ns
Turn-On Rise Time	t _r	V_{DD} =-10V, I_{D} =-3.1A, V_{GS} =-4.5V,RG=6 Ω	-	4	-	ns
Turn-Off Delay Time	t _{d(off)}		-	34	-	ns
Turn-Off Fall Time	t _f		-	5	-	ns
	Dre	ain-Source Diode				
Maximum Continuous Drain-Source	I _S	- Jource Dioue	_	_	-1.5	А
Diode Forward Voltage	V _{SD}	I _S =-1.0A, V _{GS} =0V		_	-1.2	V
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Note:

- 1. Pulse width<300us, Duty cycle<2%
- 2. Fused current that based on wire numbers and diameter
- 3. Guaranteed by design, not subject to production testing.



TYPICAL CHARACTERISTIC CURVES

Fig.1 On-Region Characteristics

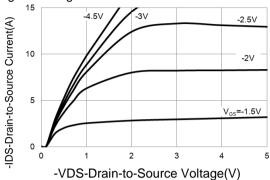


Fig.2 Transfer Characteristics

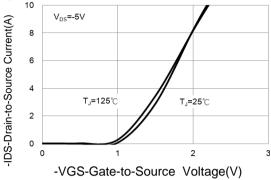


Fig.3 On-Resistance vs. Drain Current

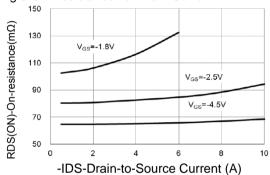


Fig.4 On-Resistance vs.Junction temperature

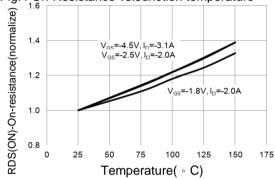


Fig.5 On-Resistance Variation with VGS.

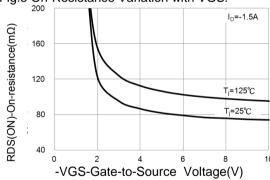


Fig.6 Body Diode Characteristics

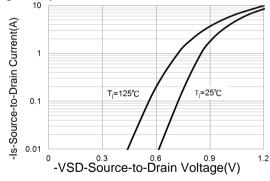


Fig.7 Gate-Charge Characteristics

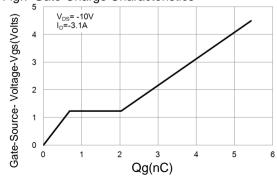
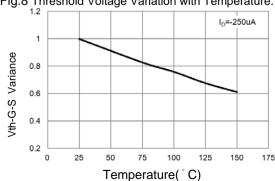
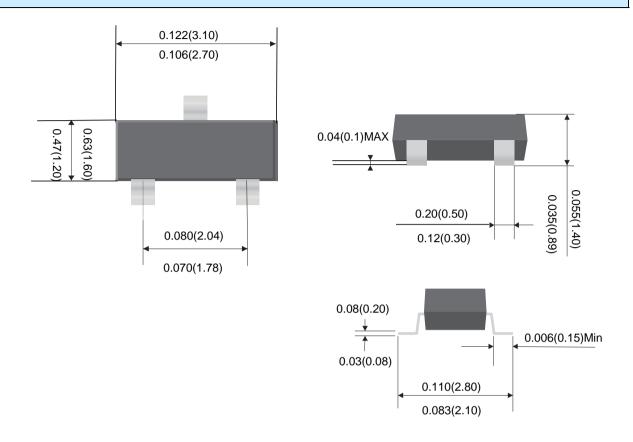


Fig.8 Threshold Voltage Variation with Temperature.

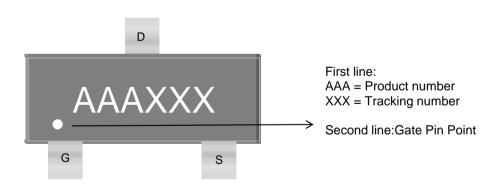




Package Outline Dimensions (inches and millimeters)



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