$I_D(A)$

-5.7

-5.3

-4.6



Preliminary Datasheet

PRODUCTY SUMMARY

Rdson @-10V

Rdson @-4.5V

Rdson @-2.5V

 $R_{DS(on)} m(\Omega)$

P-Channel 30-V (D-S) MOSFET

FEATURES

- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

Aρ	plication	

- Portable Devices
- ■Consumer Electronics

55

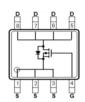
63

86

 V_{DS}

-30

SOP-8-Single



Mechanical

● Case: SOP-8-Single Package

Packing Information

Package	Packing
SOP-8-Single	3K/13" Reel

Maximum Ratings (T _A =25°C unless otherwise specified)						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage	V_{DS}	-30	V			
Gate-Source Voltage	V_{GS}	±12	V			
Continuous Drain Current 1)	I _D	-5.7	Α			
Maximum Power Dissipation	P _D	1.8	W			
Pulsed Drain Current 2)	I _{DM}	-22.8	Α			
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C			

Typical Thermal Resistance					
Parameter	Symbol	Limit	Unit		
Junction-to-Ambient Thermal Resistance	$R_{\theta JA}$	110	°C/W		

Note:

R0JA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper



Electrical Ch	aracteristi	CS (T _A = 25°C UNLESS OTH	ERWISE	NOTED)			
Charactaristics	Complete	Took Condition	Limits				
Characteristics	Symbol Test Condition		Min	Тур	Max	Unit	
		Static					
Drain-Source Breakdown Voltage	B _{VDSS}	VGS = 0V, I _D =250uA	-30			V	
Gate Threshold Voltage	V _{GS(th)}	VDS=VGS, I _D =250uA	-0.50	-1.00	-1.30	V	
		V _{GS} =-10.0V, I _D =-5.7A	-	45	55	mΩ	
Drain-Source On-State Resistance	R _{DS(on)}	VGS=-4.5V, ID=-5.3A	-	52	63	mΩ	
		VGS=-2.5V, ID=-4.6A	-	71	86	mΩ	
Zero Gate Voltage Drain Current	I _{DSS}	VDS=-30V, VGS=0V			1.0	uA	
Gate-Source Leakage Current	I _{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$			±100	nA	
		Dynamic ³⁾					
Total Gate Charge	Q_g		-	12	-	nC	
Gate-Source Charge	Q_gs	V_{DS} =-10V, I_{D} =-3.1A, V_{GS} =-4.5V	ı	1.7	-	nC	
Gate-Drain Charge	Q_{gd}	- 66	1	2.3	-	nC	
Input Capacitance	C _{iss}			528	-	pF	
Output Capacitance	C _{oss}	V_{DS} =-10V, V_{GS} =0V, f=1.0MHZ	1	63	-	pF	
Reverse Transfer Capacitance			1	48	-	pF	
		Switching					
Turn-On Delay Time	t _{d(on)}		-	5	-	ns	
Turn-On Rise Time	t _r	V_{DD} =-10V, I_{D} =-3.1A,	-	33	-	ns	
Turn-Off Delay Time	t _{d(off)}	V _{GS} =-4.5V,RG=6Ω	-	27	-	ns	
Turn-Off Fall Time	t _f		ı	10	-	ns	
	Dra	ain-Source Diode			,		
Maximum Continuous Drain-Source	I _S	-	-	-	-1.5	Α	
Diode Forward Voltage	V_{SD}	I _S =-1.0A, V _{GS} =0V	-	-	-1.2	V	

Note:

- 1. Pulse width<300us, Duty cycle<2%
- 2. Fused current that based on wire numbers and diameter
- 3. Guaranteed by design, not subject to production testing.



Package Outline Dimensions (inches and millimeters)

		SOP-8	}		
		Dime	nsions		
SYMBOL	Millimeters		Inch	ies	
	Min	Max	Min	Max	
Α	-	1.75		0.069	
A1	0.10	0.23	0.004	0.009	
b	0.35	0.48	0.014	0.019	
С	0.19	0.25	0.007	0.010	l l l e l
D	4.70	5.10	0.185	0.201	a° a°
Е	5.80	6.20	0.228	0.244	D
E1	3.70	4.10	0.146	0.161	
е	1.27bsc			•	
L	0.50	0.80	0.020	0.031	AI
a °	0 °	8 °	0 °	8 °	

AAAA XXXXXXX First line: AAAAA = Product number XXXXXXXX = Tracking number Third line:Gate Pin Point

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