

Preliminary Datasheet

N Channel 30V (DS) MOSFET

FEATURES

- Advance Trench Process Technology
- High Density Cell Design for Ultra Low Onresistance

PRODUCTY SUMMARY					
V _{DS}	R	$_{DS(on)} m(\Omega)$	I _D (A)		
30	7.5	@V _{GS} =10V	20		
30	12	@V _{GS} =4.5V	16		

Application

- ●Portable Devices
- ■Consumer Electronics

TO-252

Mechanical





Packing Information

●Case: TO-252 Package

Package	Packing		
TO-252	2.5K/13" Reel		

Maximum Ratings (T _A =25°C unless otherwise specified)						
Parameter	Symbol	Limit	Unit			
DrainSource Voltage	V _{DS}	30	V			
GateSource Voltage	V _{GS}	±20	V			
Continuous Drain Current 1)	I _D	20	Α			
Maximum Power Dissipation	P _D	3.2	W			
Pulsed Drain Current 2)	I _{DM}	80	Α			
Operating Junction and Storage Temperature Range	T_J, T_{STG}	55~150	°C			

Typical Thermal Resistance					
Parameter	Symbol	Limit	Unit		
JunctiontoAmbient Thermal Resistance 3)		100	°C/W		

Note:

- 1. Fused current that based on wire numbers and diameter
- 2. Repetitive Rating: Pulse width limited by the maximum junction temperature
- 3. 1in2 2oz Cu PCB board



Electrical C	haracteristi	CS (T _A = 25°C UNLESS OTH	ERWISE	NOTED)		
Characteristics	Compleal		Limits			
Characteristics	Symbol	Test Condition	Min	Тур	Max	Unit
		Static				
OrainSource Breakdown Voltage	B _{VDSS}	$V_{GS} = 0V, I_{D} = 250uA$	30	-	-	V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250uA$	1.0	2.0	3.0	V
Proin Course On Ctate Desistance	В	V _{GS} =10V, I _D =20A	-	6.4	7.5	mΩ
OrainSource OnState Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =16A	-	10	12	mΩ
Zero Gate Voltage Drain Current	I _{DSS}	VDS=30V, VGS=0V	-	-	1	uA
GateSource Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA
	•			•		
		Dynamic 3)				
Total Gate Charge	Q _g		-	22	-	nC
GateSource Charge	Q_{gs}	$V_{DS} = 15V, V_{GS} = 0V, I_{D} = 20$	-	4.5	-	nC
GateDrain Charge	Q_{gd}		-	4	-	nC
nput Capacitance	C _{iss}		-	1219	-	pF
Output Capacitance	C _{oss}	V_{DS} =15V, V_{GS} =0V,f=1M H_{Z}	-	182	-	pF
Reverse Transfer Capacitance	C _{rss}		-	88	-	pF
	•	-		!	-	
		Switching				
urnOn Delay Time	t _{d(on)}		-	13	-	ns
urnOn Rise Time	t _r	V _{DS} =15V,V _{GS} =10V,Rg=	-	10	-	ns
TurnOff Delay Time	t _{d(off)}	24Ω,RL=15Ω, I_D =1A	-	46	-	ns
TurnOff Fall Time	t _f		-	7	-	ns
	<u> </u>					
	Dr	ainSource Diode				
Maximum Continuous Body Diode Forward Current	I _S	V _G =V _D =0V , Force Current	-	-	1.2	Α
Diode Forward Voltage	V_{SD}	IS=1.0A, VGS=0V	-	-	1.5	V

NOTES:

NOTES:

1. Pulse width<300us, Duty cycle<2%.

2. Essentially independent of operating temperature typical characteristics.

3. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.

4. The maximum current rating is package limited.

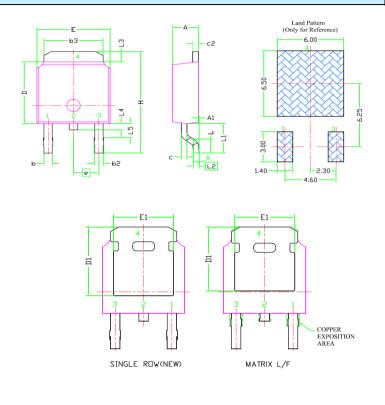
5. RQJA is the sum of the junctiontocase and casetoambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch2 with 2oz.square pad of copper.

6. Guaranteed by design. not subject to production testing.

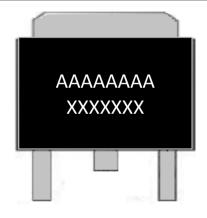


Package Outline Dimensions (inches and millimeters)

TO-252						
	Dimensions					
SYMBOL	Millimeters		Inches			
	Min	Max	Min	Max		
Е	6.40	6.73	0.252	0.265		
L	1.40	1.77	0.055	0.070		
L1		2.743	REF			
L2		0.508	BSC			
L3	0.89	1.27	0.035	0.050		
L4	0.64	1.01	0.025	0.040		
L5	-	-	-	-		
D	6.00	6.22	0.236	0.245		
Н	9.40	10.40	0.370	0.409		
b	0.64	0.88	0.025	0.035		
b2	0.77	1.14	0.030	0.045		
b3	5.21	5.46	0.205	0.215		
е	2.286BSC					
Α	2.20	2.38	0.087	0.094		
A1	0.00	0.13	0.000	0.005		
С	0.46	0.60	0.018	0.024		
c2	0.46	0.58	0.018	0.023		
D1	5.21	-	0.205	-		
E1	4.40	-	0.173	-		
Θ	0 °	10 °	0 °	10 °		



Marking Information



First line:

AAAAAAA = Product number

XXXXXXX = Tracking number

Third line:Gate Pin Point

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