

V2.1 Datasheet

N-Channel 30-V (D-S) MOSFET

FEATURES

- ●Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

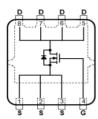
PRODUCTY SUMMARY						
V_{DS}	$R_{DS(on)} m(\Omega) Max$					
30	3.5	@V _{GS} =10V				
30	4.8	@V _{GS} =4.5V				

Application

- ■Portable Devices
- ■Consumer Electronics

DFN5060





Mechanical

●Case:DFN5060 Package

Packing Information

Package	Packing
DFN5060	3K/13" Reel

Maximum Ratings (T _A =25°C unless otherwise specified)								
Parameter	Symbol	Limit	Unit					
Drain-Source Voltage	V _{DS}	30	V					
Gate-Source Voltage	V_{GS}	±20	V					
Continuous Drain Current 1)	I _{DSM}	20	А					
Continuous Drain Current 4)	I _D	80	А					
Continuous Drain Current 5)	I _{DM}	236	А					
Maximum Power Dissipation	P _D	6	W					
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C					

Typical Thermal Resistance							
Parameter Symbol Limit Unit							
Junction-to-Ambient Thermal Resistance 3)	$R_{\theta JA}$	65	°C/W				

Note:

- 1. Pulse width<300us, Duty cycle<2%.
- 2. Fused current that based on wire numbers and diameter.
- 3. Guaranteed by design, not subject to production testing.
- 4. The maximum current rating is package limited.
- 5. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$ =150°C. Ratings are based on low frequency and duty cycles to keepinitial T_J =25°C.



Electrical Characteristics (T _A = 25°C UNLESS OTHERWISE NOTED)						
Characteristics	Currele el	Test Condition	Limits			l lm:4
	Symbol	rest Condition	Min	Тур	Max	Unit
Static						
Drain-Source Breakdown Voltage	B _{VDSS}	V_{GS} =0V, I_D =250uA	30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250uA$	1.0	1.6	3.0	V
Drain Course On State Begintance	R _{DS(on)}	V_{GS} =10V, I_{D} =20.0A	-	2.8	3.5	mΩ
Drain-Source On-State Resistance		V_{GS} =4.5V, I_{D} =20.0A	-	4.0	4.8	mΩ
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	-	1.0	uA
GateSource Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA

Dynamic ³⁾						
Total Gate Charge	Q_g	V_{DS} =15V, V_{GS} =10V, I_{D} =1.5A	-	68	-	nC
Total Gate Charge	Q_g	4577 700 457	-	34	-	nC
Gate-Source Charge	Q_{gs}	V _{DS} =15V, VGS=4.5V, I _D =1.5A ^(Note 1,2)	-	6	-	nC
Gate-Drain Charge	Q_{gd}	10-1.07	-	15	-	nC
Input Capacitance	C _{iss}		-	3687	-	pF
Output Capacitance	C _{oss}	V_{DS} =15V, V_{GS} =0V, F=200kHz	-	413	-	pF
Reverse Transfer Capacitance	C _{rss}		-	361	-	pF

Switching						
Turn-On Delay Time	t _{d(on)}	\/dd 45\/	-	18	-	ns
Turn-On Rise Time	t _r	Vdd=15V, Rload=10Ohm,	-	14	-	ns
Turn-Off Delay Time	t _{d(off)}	Vgen=10V, Rg=3Ohm	-	69	-	ns
Turn-Off Fall Time	t _f	119–301111	-	14	-	ns

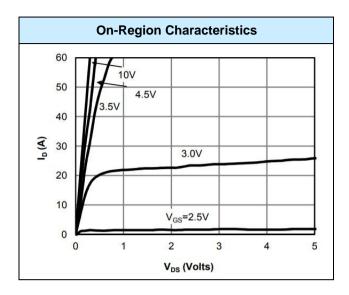
Drain-Source Diode						
Maximum Continuous Body Diode Forward Current	Is	-	-	-	1.2	А
Diode Forward Voltage	V_{SD}	I _S =1.0A, V _{GS} =0V	-	-	1.5	V

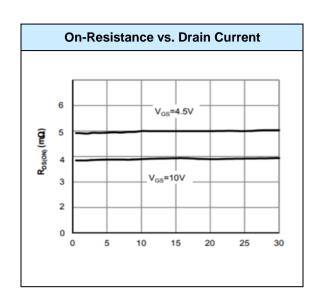
Note:

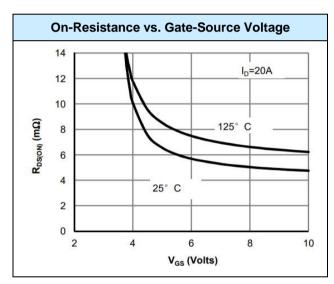
- 1. Pulse width<300us, Duty cycle<2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$ =150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 4. The maximum current rating is package limited.
- 5. R_{QJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch2 with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.

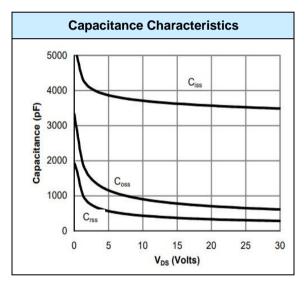


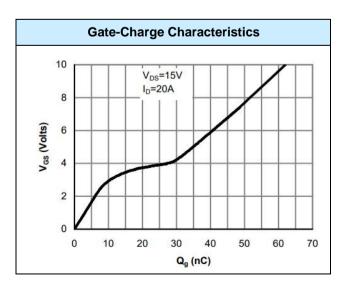
Typical Characteristic Curves







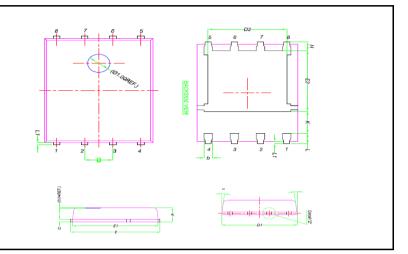




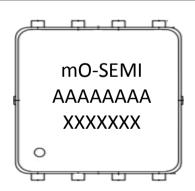


Package Outline Dimensions (inches and millimeters)

DFN5060								
	Dimensions							
SYMBOL	Millin	neters	Inc	hes				
	Min	Max	Min	Max				
Α	0.90	1.10	0.04	0.04				
A1	0.00	0.05	0.00	0.00				
b	0.33	0.51	0.01	0.02				
С	0.20	0.30	0.01	0.01				
D1	4.80	5.00	0.19	0.20				
D2	3.61	3.96	0.14	0.16				
Е	5.90	6.10	0.23	0.24				
E1	5.70	5.80	0.22	0.23				
E2	3.38	3.78	0.13	0.15				
е		1.27	'BSC					
Н	0.41	0.61	0.02	0.02				
K	1.10	-	0.04	-				
L	0.51	0.71	0.02	0.03				
L1	0.06	0.20	0.00	0.01				
۰	0 °	12 °	0 °	12 °				



Marking Information



First line = Company name

AAAAAAA = Product number

XXXXXXX = Tracking number

Fourth line = Pin1 Point

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