

MSN0130N

V1.3 Datasheet

N-Channel 30V MOSFET

FEATURES

- Advance Trench Process Technology
- ●High Density Cell Design for Ultra Low On-resistance

Application

Portable Devices

Consumer Electronics

Mechanical

●Case:DFN5060 Package

Packing Information

Package	Packing
DFN5060	3K/13" Reel

Maximum Ratings (T _A =25°C unless otherwise specified)						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage	V _{DS}	30	V			
Gate-Source Voltage	V _{GS}	±20	V			
Continuous Drain Current ¹⁾	I _{DSM}	20	А			
Continuous Drain Current ⁴⁾	Ι _D	150	А			
Continuous Drain Current ⁵⁾	I _{DM}	540	А			
Maximum Power Dissipation	P _D	65	W			
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to 175	°C			

Typical Thermal Resistance						
Parameter	Limit	Unit				
Junction-to-Ambient Thermal Resistance 3)	$R_{ extsf{ heta}JA}$	45	°C/W			

Note:

1. Pulse width<300us, Duty cycle<2%.

2. Fused current that based on wire numbers and diameter.

3. Guaranteed by design, not subject to production testing.

4. The maximum current rating is package limited.

5. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^{\circ}$ C. Ratings are based on low frequency and duty cycles to keepinitial $T_{J}=25^{\circ}$ C.

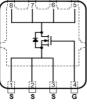


PRODUCTY SUMMARY



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 V_{DS}



 $R_{DS(on)} m(\Omega) Max$



Electrical Characteristics (T _A = 25°C UNLESS OTHERWISE NOTED)							
Characteristics	Symbol	Test Condition	Limits			11 14	
			Min	Тур	Max	Unit	
Static							
Drain-Source Breakdown Voltage	B _{VDSS}	V_{GS} =0V, I_{D} =250uA	30	-	-	V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250$ uA	1.0	1.7	3.0	V	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20.0A	-	-	1.75	mΩ	
		V _{GS} =4.5V, I _D =20.0A	-	-	2.8	mΩ	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =30V, V_{GS} =0V	-	-	1.0	uA	
GateSource Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA	

Dynamic ³⁾						
Total Gate Charge	Q _g	V _{DS} =15V, V _{GS} =10V, I _D =1.5A	-	61.7	-	nC
Total Gate Charge	Qg	V _{DS} =15V, V _{GS} =4.5V, I _D =1.5A ^(Note 1,2)	-	30.1	-	nC
Gate-Source Charge	Q _{gs}		-	8.1	-	nC
Gate-Drain Charge	Q _{gd}		-	11.7	-	nC
Input Capacitance	C _{iss}	V _{DS} =15V, V _{GS} =0V, f=200KHz	-	4558	-	pF
Output Capacitance	C _{oss}		-	910	-	pF
Reverse Transfer Capacitance	C _{rss}		-	341	-	pF

Switching							
Turn-On Delay Time	t _{d(on)}		-	23.4	-	ns	
Turn-On Rise Time	t _r	V _{DS} =15V, Rload=10Ohm, Vgen=10V, Rg=3Ohm	-	12.9	-	ns	
Turn-Off Delay Time	t _{d(off)}		-	87.9	-	ns	
Turn-Off Fall Time	t _f		-	46.8	-	ns	

Drain-Source Diode						
Maximum Continuous Body Diode Forward Current	ا _s	-	-	-	1.2	А
Diode Forward Voltage	$V_{\rm SD}$	I _S =1.0A, V _{GS} =0V	-	-	1.5	V

Note:

1. Pulse width<300us, Duty cycle<2%.

2. Essentially independent of operating temperature typical characteristics.

3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$ =150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.

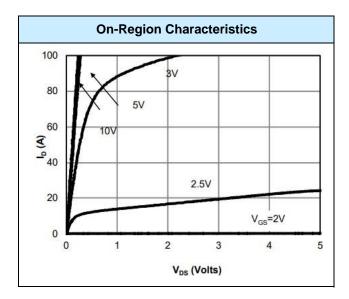
4. The maximum current rating is package limited.

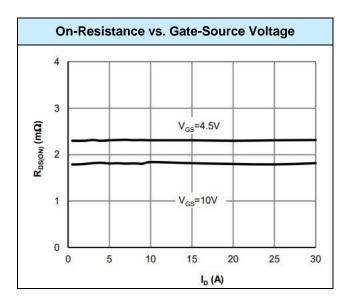
5. R_{QJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch2 with 2oz.square pad of copper.

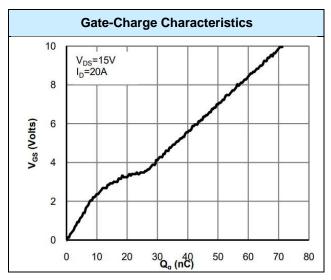
6. Guaranteed by design, not subject to production testing.

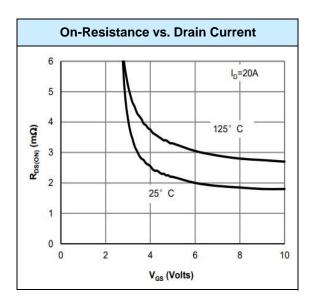


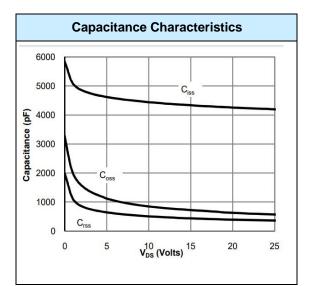
Typical Characteristic Curves





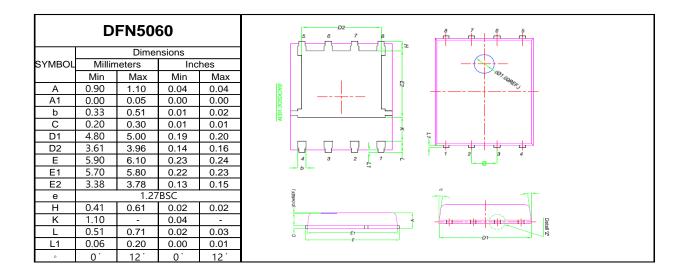








Package Outline Dimensions (inches and millimeters)



 Marking Information

 First line = Company name

 AAAAAAAA

 AAAAAAAAA

 XXXXXXXX

 First line = Company name

 AAAAAAAAA

 XXXXXXXX

 Fourth line = Pin1 Point

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